



Lubrication of polycrystalline silicon MEMS via a thin silicon carbide coating

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ARTICLE INFO

Article history:

Received 31 August 2011

Received in revised form

21 September 2012

Accepted 15 January 2013

Available online 24 January 2013

Keywords:

MEMS

High aspect ratio structures

Polycrystalline silicon

Silicon carbide

Solid lubrication

Stiction

Surface modification

Wear resistance

ABSTRACT

The contacting surfaces of a microfabricated polycrystalline silicon (polysilicon) double-clamped beam adhesion test structure have been modified with a thin ultra-hard, wear-resistant, and conformal silicon carbide (SiC) film. Adhesion forces in SiC-coated interfaces as a function of apparent area of contact have been determined quantitatively and compared with those in uncoated polysilicon contacts. Furthermore, contact reliability studies have been carried out by following the changes in physicochemical properties of the surfaces after >100 billion contact cycles. The results highlight the tribological benefits of SiC coating as a solid lubricant in devices undergoing cyclic contacts.

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1. Introduction

Successful deployment of silicon-based microelectromechanical systems (MEMS) necessitates proper passivation and lubrication of the contacting surfaces to prevent irreversible interfacial adhesion (*stiction*) and wear [1–4]. Unwanted adhesion, more commonly known as *stiction*, remains a common failure mechanism in silicon-based microelectromechanical systems and in advanced microelectronics with high aspect ratio structures due to a variety of factors, including propensity of silicon to form a layer of hydrophilic, high surface energy native oxide [2,5]. Although release-related *stiction* has been addressed through several schemes including vapor HF [6–9], critical point drying [10–13], by roughening the surfaces [14,15], or by self-assembled monolayer coating schemes [16–21], in-use *stiction* still poses a significant issue to the reliability of MEMS. This phenomenon occurs when surface forces such as van der Waals, capillary, and/or electrostatic force dominate over the restoring force and causes a malfunction during device operation. The rough contacting surfaces of microfabricated structures built out of polysilicon have been also shown to be mechanically weak due to high contact pressures generated at the asperity contacts. As a consequence,

it is expected that silicon microstructures designed for repetitive contacts during operation likely fail due to wear which severely limits the lifetime of the devices. Additionally, silicon-based MEMS are challenged for operating under harsh environmental conditions of elevated temperatures, oxidative and corrosive environments, and in mechanically demanding applications, e.g., for combustion monitoring and power generation [22–25].

One effective way to resolve these reliability issues is by coating the wafers with a thin silicon carbide layer across the surface to reduce the surface energy of the device [26]. SiC is a material of choice for solid lubrication in polysilicon MEMS due to its many outstanding properties, including the ability to tailor its doping level to enhance conductivity and minimize charging, high specific strength and creep resistance, thermal stability and conductivity, chemical inertness, mechanical strength (hard), wear resistance, low surface energy, hydrophobic, and the synthesis ability for conformal coating of surfaces that are not in line of sight. The incorporation of ultra-hard, wear-resistant, and conformal SiC hard coatings in MEMS architecture takes advantage of these attractive properties and at the same time allows the preservation of the overall microsystem design.

With the introduction of a new tribological interface, that of the SiC coating that interacts directly with adsorbates or lubricant species present in the operating environment, it is desirable to study and predict the failure mechanisms sooner than they occur in normal MEMS operation to pave the way for full scale implementation of MEMS to the mainstream. Experimentally, adhesion forces in polycrystalline SiC (polySiC) interface as a function

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of apparent contact area have been determined quantitatively using a microfabricated test structure. This paper also presents a detailed, systematic study of the evolution of physicochemical properties of the interface for over 100 billion contact cycles. The correlated results of adhesion and lifetime studies are then compared to those obtained on polysilicon interfaces with comparable topography.

2. Experimental details

2.1. Device structure, fabrication and surface modification

An array of double-clamped beam (DCB) test structures designed to measure adhesion forces in micrometer-scale surface interactions were fabricated through standard PolyMUMPS (MEMSCAP, Inc.) multilayer micromachining processes. The fabrication processes including the release procedure are described in detail in Ref. [27]. Fig. 1(a)–(c) depicts the schematic top and side views of the fabricated DCB test structure illustrating both non-contact and contact states. The beam (also called source electrode by convention) is suspended $2\ \mu\text{m}$ above a landing pad (drain electrode) and two symmetrically positioned actuation pads through which the actuation (bias) voltage is applied. A representative scanning electron microscopy (SEM) image of a DCB device with $275\ \mu\text{m}$ long beam is shown in Fig. 1(d). The beam and the landing pad are both grounded. An extra probe pad attached to the drain electrode is designed to enable four-point current vs. voltage (I – V) measurements. A dimple with $0.75\ \mu\text{m}$ thickness is incorporated in the middle of the beam and directly above landing pad to define the apparent area of contact. The separation between the dimple and the landing pad is $1.25\ \mu\text{m}$.

A series of DCB test structures with 275 and $350\ \mu\text{m}$ long beams and with dimple sizes in the range from $4\ \mu\text{m} \times 4\ \mu\text{m}$ to $12\ \mu\text{m} \times 12\ \mu\text{m}$ were used for this study. The devices were released [27] and then coated with SiC film. The SiC layer was deposited on the released structures via low pressure chemical vapor deposition (LPCVD) using disilabutane (DSB) precursor. The LPCVD reactor (TekVac) was pumped down to a base pressure of $<5 \times 10^{-6}$ Torr. The gas phase DSB reagent was introduced at a flow rate of $5\ \text{sccm}$ for $4\ \text{min}$ at a reactor temperature of 780°C . The resultant SiC layer has a thickness of $50\ \text{nm}$ as determined by prior calibration done on a separate flat Si(100) substrate and verified through AFM measurements. The film also yielded a smoother surface with an average root-mean-square (rms) roughness of $10.2\ \text{nm}$ in comparison with as-released polysilicon substrate which has an rms roughness of $\sim 15.1\ \text{nm}$. After SiC layer deposition, the separation between the beam and a landing pad is decreased from $1.25\ \mu\text{m}$ to $1.15\ \mu\text{m}$. A bias of $\sim 75\ \text{V}$ is typically required to actuate a $275\ \mu\text{m}$ long SiC-coated DCB into contact with the landing pad, whereas polysilicon DCB of the same length requires $\sim 25\ \text{V}$. The DCB design was chosen to ensure symmetric contact and enable precise measurements of adhesion forces using a methodology outlined in Refs. [27,28].

2.2. Measurement of adhesion forces

Adhesion measurements using DCB devices were carried out in a similar scheme to that reported previously [27,28]. For direct comparison, similar measurements were done on as-fabricated (native oxide-coated) polysilicon structures and on SiC-coated structures. Optical interferometric techniques have been used to systematically determine the beam profile and the pull-in and pull-off voltage

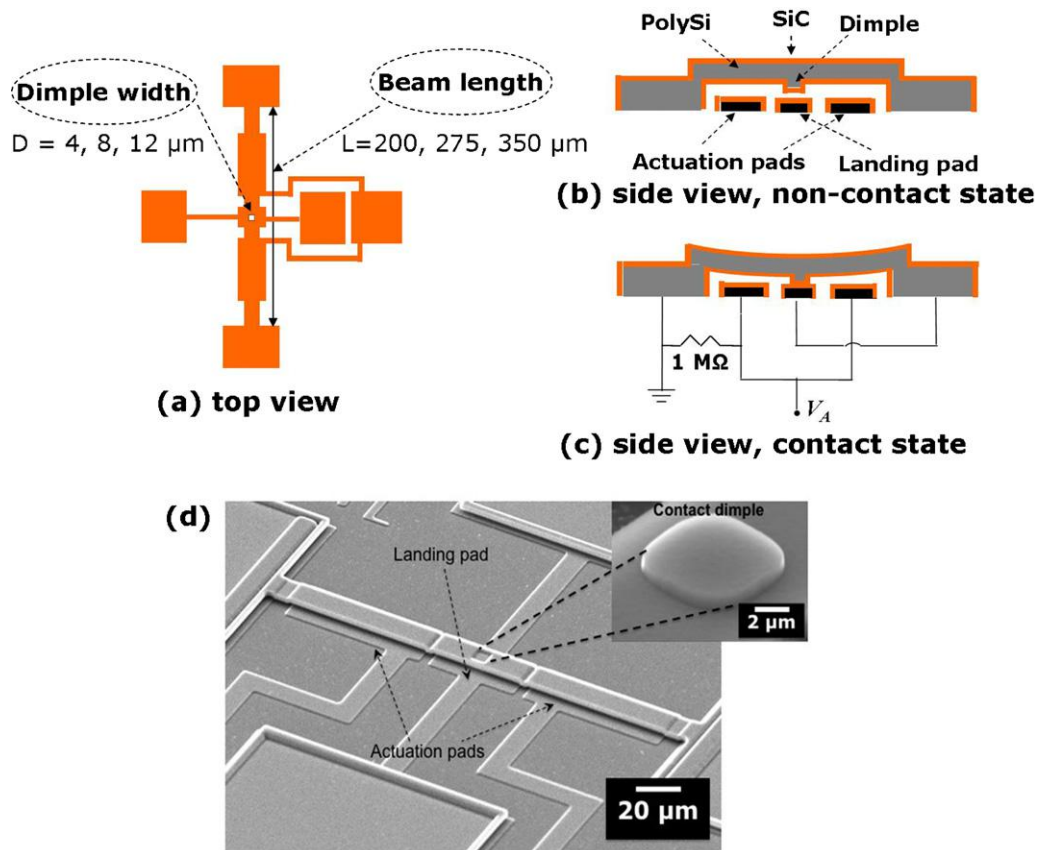


Fig. 1. (a)–(c) Schematic representations of the electrostatically actuated double-clamped beam test structure. The same designs were also coated with a polycrystalline SiC film for comparison. (d) Representative SEM micrograph of a $275\ \mu\text{m}$ long polysilicon DCB device (inset is an inverted dimple).

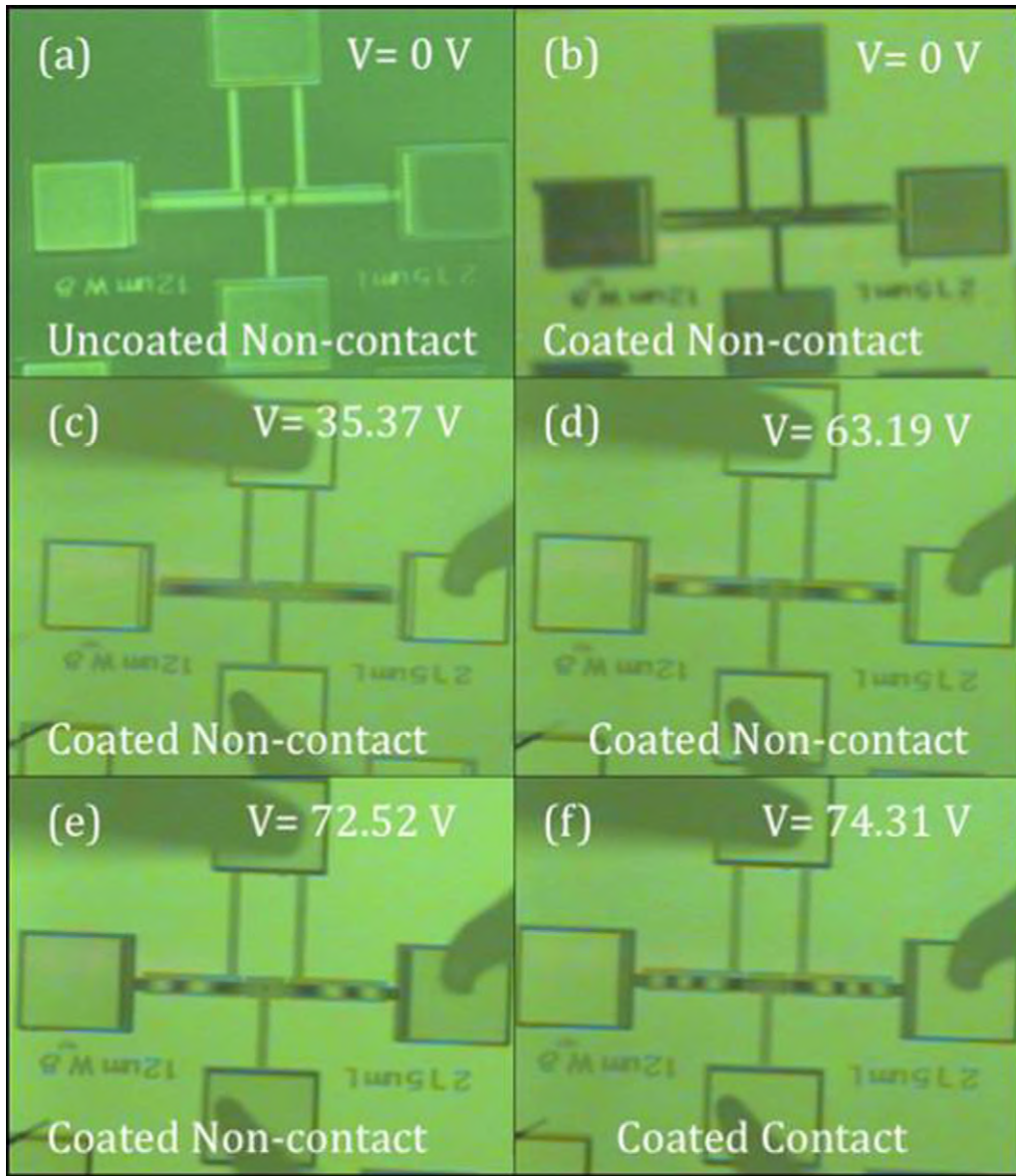


Fig. 2. (a) Interferometric image of as-fabricated polysilicon test structure. (b)–(f) Interferometric images of SiC-coated structure at various applied voltages as the beam was brought into contact with the landing pad electrostatically.

conditions for the DCB adhesion test structures. Fig. 2 shows the DCB device as fabricated (a) and after SiC coating (b) and at various applied voltages (c–f). The interferometric image of the beam before any applied bias (Fig. 2b) shows that residual strain upon SiC coating is minimal and has little to no effect in the calculation of cantilever spring constant. Fig. 3 summarizes the beam profile vs. applied bias deduced from interferometric images.

The adhesion force between the contacting surfaces is calculated as follows:

$$F_{adh} = k\Delta z - \left(\frac{1}{2}\right) w\epsilon_0 V_{pull-off}^2 \int_0^x \frac{dx}{g^2(x)} \quad (1)$$

where k represents the cantilever spring constant, Δz is the beam displacement during substrate contact, w is the actuation electrode width, ϵ_0 is permittivity of air, $V_{pull-off}$ is the voltage at which the beam releases from contact, and g is the gap distance between the beam and landing pad at the actuation pad length, x . The first term represents the cantilever spring restoring force, and the second

term is the opposing electrostatic force acting on a deformable capacitor.

2.3. Lifetime studies

To study the evolution of microscale contacts as a function of number of impact cycles, two separate as-fabricated polysilicon and SiC-coated devices with $12 \mu\text{m} \times 12 \mu\text{m}$ contact dimples were each cycled at 60 kHz frequency through a sinusoidal voltage and an applied load of 5 V overdrive equivalent to $8 \mu\text{N}$ contact force, estimated using finite element simulation methods. The cyclic contact experiments were performed in an enclosed, airtight chamber at 25 °C and ~50% relative humidity. The humidity was maintained by purging the enclosed probe station (Signatone S-1160) continuously with dry air. A 50% humidity was chosen due to the fact that most Si-based microdevices operate under ambient atmospheric conditions. The devices were wirebonded using Al microwires and the actuation voltage (AC bias) was supplied from a home-built function generator. Longer devices were deliberately chosen for

lifetime studies on SiC-coated structures to bring the actuation voltage to <100 V in order to reduce charging effects from otherwise high electric fields. A 275 μm long polysilicon DCB and a 350 μm long SiC-coated DCB have comparable actuation voltages of ~ 25 V. Contact resistance vs. number of impacts was monitored through I - V measurements at set periods of cycling using a Keithley 2400 Sourcemeter instrument by the four-point method.

After cycling the devices to ~ 100 billion times, the suspended beams were removed to allow analyses of the impacted surfaces. The surface morphology and surface potential postmortem were characterized via atomic force microscopy (AFM) and Kelvin probe force microscopy (KPFM) through a Digital Instrument Nanoscope IIIa scanning probe microscope equipped with extender electronics module. Finally, chemical analysis of the impacted landing pads was accomplished through Auger electron spectroscopy (AES) and microscopy methods using a Physical Instruments, Inc. PHI 700 Scanning Auger Nanoprobe system. For comparison, a separate set of unactuated devices on the same die were removed and analyzed similarly.

3. Results and discussion

3.1. Adhesion force measurements

Fig. 4 summarizes the measured adhesion force for polycrystalline Si/Si and SiC/SiC contacts as a function of dimple area. As mentioned in the experimental section, the adhesion force for a given dimple size is calculated from the difference between the restoring and the electrostatic forces at pull-off. The former requires accurate determination of the spring constant from the actual beam profile while the latter involves measurement of the distributed gap between the beam and the actuation pads just prior to the beam separating from contact with the landing pad. The results demonstrate that polycrystalline SiC/SiC contacts exhibit lower adhesion compared to polycrystalline Si/Si despite the lower roughness of the SiC surfaces (10.2 nm vs. 15.1 nm for polysilicon). It is expected for rougher surfaces to exhibit lower adhesion forces due to reduced real contact area. The opposite trend observed here highlights the tribological benefit of intrinsically low surface energy and high hardness of SiC. The overall results also suggest that the adhesion force between rough, rigid surfaces scales weakly with the apparent contact area. The expected linear scaling with apparent area is still not seen in smoother SiC surfaces which highlights

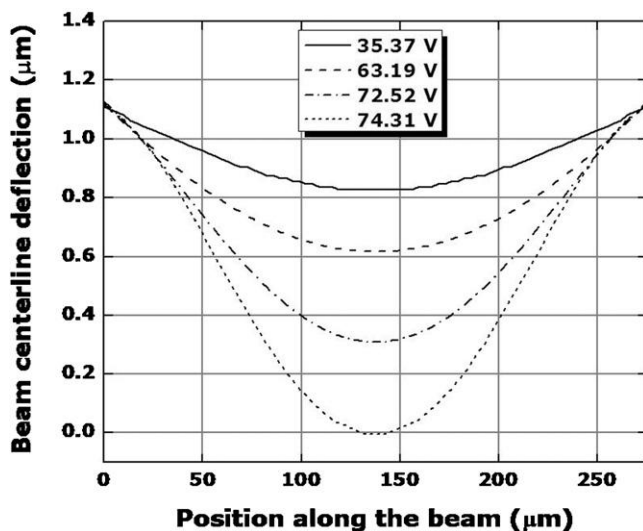


Fig. 3. Beam deflection profile deduced from interferometric image for 275- μm SiC-coated device at various applied voltages.

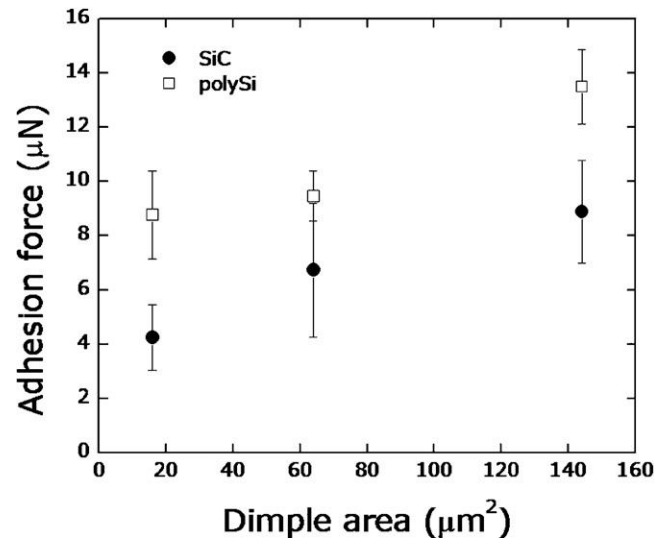


Fig. 4. Plot of adhesion force as a function of dimple area. Polycrystalline SiC/SiC-coated contacts exhibit lower adhesion compared to polycrystalline Si/Si.

that real contacts in rough surfaces involve only a few asperities [2,28].

It must be noted that the magnitude of adhesion force for a given dimple size in this study is generally lower compared to the previous polyMUMPs fabrication run [27]. The discrepancy is ascribed to roughness variation, i.e. devices in the current generation are 1.5 times rougher than the previous fabrication run reported in Ref. [27]. These findings underscore the importance of thorough characterization of interacting surfaces as different fabrication runs could yield large variations in surface morphology which is directly related to the measured adhesion force once surfaces are brought into contact.

3.2. Lifetime studies

A figure of merit for predicting MEMS reliability is based on reliable electrical and tribological performance of devices in excess

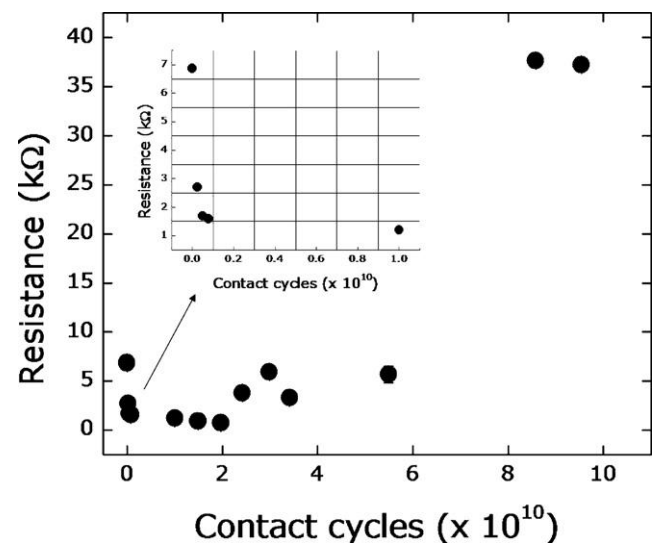


Fig. 5. Contact resistance of polySi/polySi device as a function of number of impacts. Initially during the first 10 billion cycles, contact resistance decreases as a function of number of impacts, then increases abruptly after ~ 50 billion cycles, ascribed to the generation of a single piece of wear debris (fractured grain) which breaks loose, leading to a high-resistance point contact.

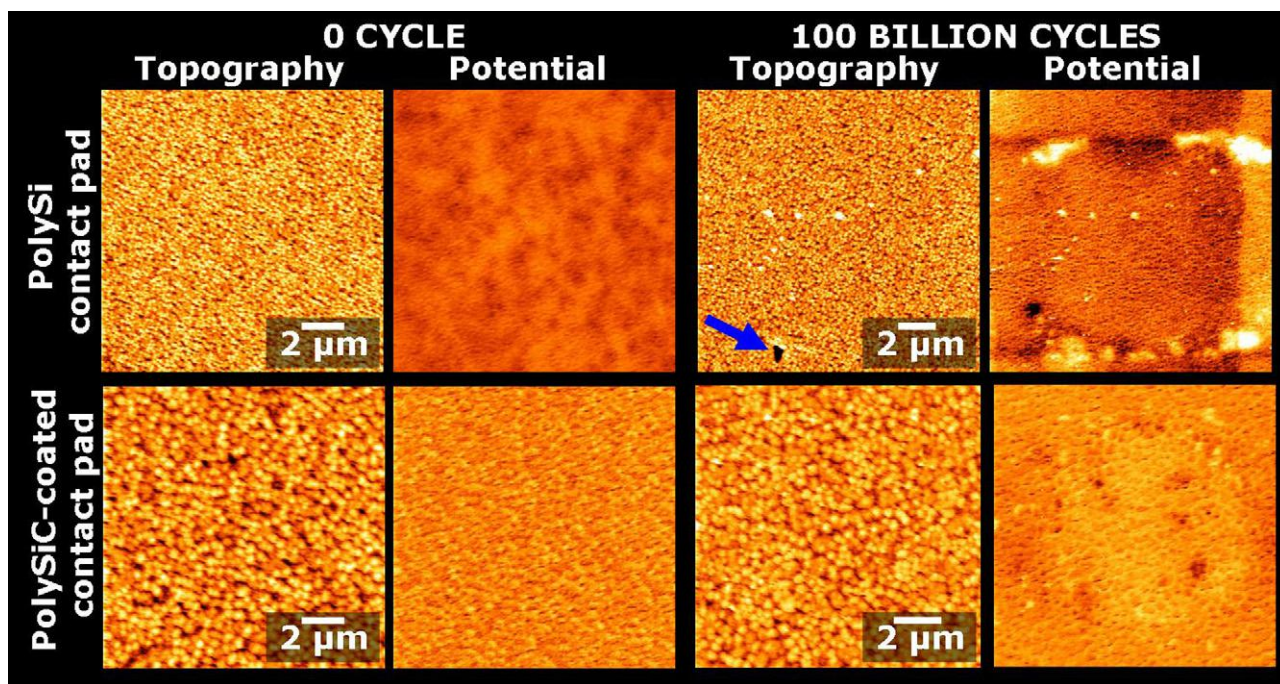


Fig. 6. AFM and KPFM images of the landing pads (top panel: polysilicon; lower panel: SiC) prior to any impact and after 100 billion cycles. An arrow is drawn to highlight a crater formed on the impacted polysilicon surface. Roughness (rms) of landing pads before any impacts: 15.1 nm (polysilicon) and 10.2 nm (polySiC). The z-scales are 500 nm and 200 mV, respectively.

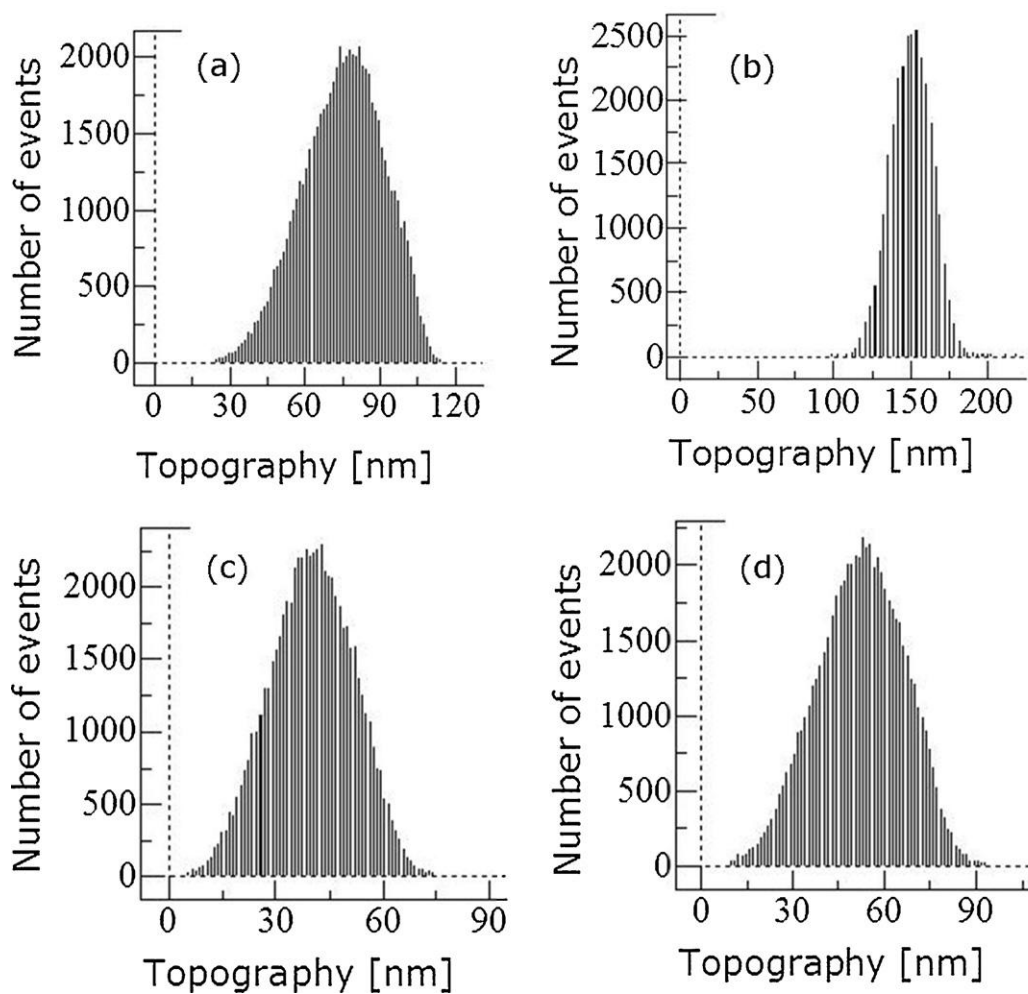


Fig. 7. Roughness histograms of the landing pads: (a) as-fabricated polysilicon, (b) polysilicon after 100 billion contacts, (c) SiC-coated polysilicon prior to any impact, and (d) SiC-coated polysilicon after 100 billion cycles.

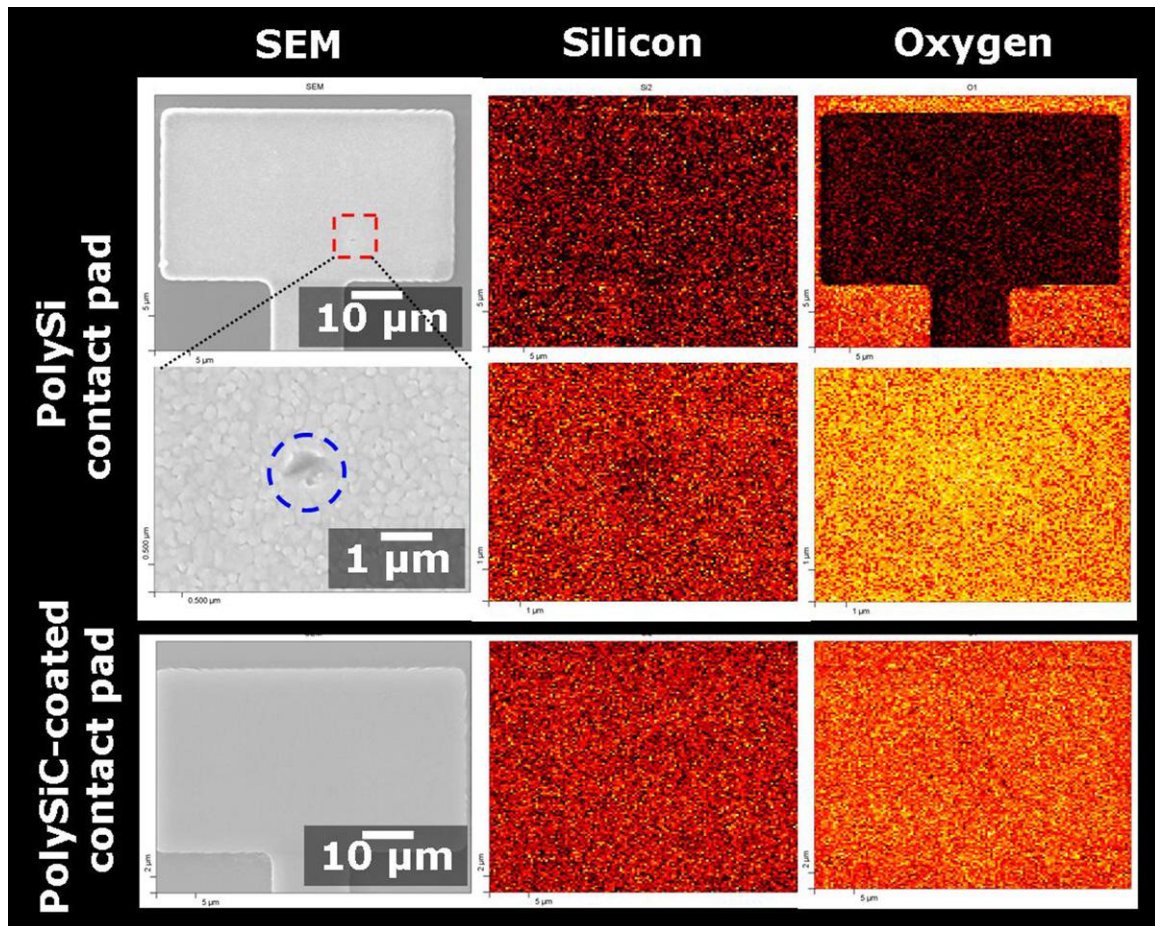


Fig. 8. Scanning Auger electron analysis on the impacted landing pads. Left panels: secondary electron image of the contact pads; middle and right panels: relative silicon and oxygen concentration maps indicating higher degree of oxidation on the worn surface of polysilicon (as indicated by more intense, bright Auger signal) after 100 billion contact cycles. A circle is drawn to highlight the worn area on polysilicon contact pad.

of tens of billions of contact cycles [29–31,33]. To assess long-term reliability, lifetime studies on SiC-coated structures and on as-fabricated polysilicon with native oxide were performed by following the microscale changes in physical, chemical, and electrical properties [35,36] of both interfaces upon cyclic loading. The inherent low surface energy of SiC afforded measurements performed using devices with longer beams without suffering from permanent adhesion.

The electrical properties of polysilicon surfaces were monitored via four-point I – V measurements and the measured contact resistance vs. number of impact cycles is summarized in Fig. 5. An initial decrease in resistance is observed, attributed to asperity flattening, and after ~ 50 billion cycles, the resistance increases abruptly, reaching to about $40\text{ k}\Omega$ after 90 billion cycles. A decrease in contact resistance at an early stage of cyclic experiments is in agreement with the behavior previously observed on polySi/polySi contact studies, albeit performed via a single-clamped, 2-axis MEMS deflecting cantilever (2-AMDC) device with $3\text{ }\mu\text{m}$ -diameter dimple size [32]. This phenomenon is ascribed to flattening of asperity summits characteristic of rough and rigid interacting surfaces. Asperity flattening typically is observed during the first few tens of million cycles for polySi surfaces [32] with roughness of $\sim 10\text{ nm}$ and even at much lower applied loads (tens of nN). The contact resistance for undoped SiC, on the other hand, caused the current to fall outside the ammeter detection limit ($<10\text{ pA}$). This has been verified at least three times throughout the duration of over 100 billion contact cycles.

After 100 billion cycles, the beams were removed to allow analyses of both impacted surfaces. Fig. 6 displays the AFM/KPFM images of the landing pad before any impact and after 100 billion cycles. Results indicate little change to the SiC surface morphology and suggest wear resistance under relatively high impact forces. In contrast, impacted polysilicon exhibited wear debris and crater-like surface features indicative of grain fracture from cyclic loading, consistent with previous studies using 2AM-DC test structure [32]. This crater-like surface reduces the effective contact area and thus increases the contact resistance measured. Surface potential images also indicate that the impacted region is chemically different after 100 billion contacts.

Fig. 7 compares the roughness histograms of as-fabricated landing pads and after 100 billion contact cycles. A comprehensive amplitude-based surface roughness analysis suggests that polysilicon became significantly rougher [by comparing the average height of grains between the un-impacted (a) $\approx 72\text{ nm}$ and postmortem (b) $=150\text{ nm}$]. The average roughness of SiC-coated landing pad, on the other hand, remain virtually unchanged before (c) and after 100 billion cycles (d).

The ratio of oxygen to silicon concentration as determined from AES analysis of the landing pad is higher in the worn areas of polysilicon (Table 1), in comparison with its unimpacted counterpart. This is consistent with the elevated surface potential observed from KPFM analysis of the impacted polysilicon surface. Higher degree of oxidation is ascribed to the removal of the native oxide layer from repetitive impacts between the asperity summits that are in direct contact. As fresh silicon surface is exposed to

Table 1
Summary of ratios atomic concentrations of oxygen to silicon ([O]/[Si]) on the interacting surfaces as determined through AES analysis.

	Polysilicon		SiC	
	Landing pad	Dimple	Landing pad	Dimple
As-fabricated	0.48	0.47	0.12	0.61
After 100 billion cycles	8.6	1.6	0.14	0.72

atmosphere, further oxidation occurs and is accelerated in worn, high surface area regions. In contrast, much lower degree of oxidation was observed in the impacted SiC landing pad.

Fig. 8 displays spatially resolved silicon and oxygen concentrations on the contact pads obtained from scanning AES. The results suggest that the slight contrast in surface potential observed in impacted SiC is not directly related to oxidation but rather may be due to surface fatigue, which affects the work function [34]. The structural changes that may exist beneath the SiC surface due to subsurface fatigue may be responsible for the observed changes in surface potential even in the absence of wear. In contrast, AES analysis reveals that oxygen concentration is higher in the worn, impacted surfaces of polysilicon (Fig. 8), which explains the elevated surface potential in the contact zone.

4. Conclusions

This work illustrates device-level measurements of adhesion forces and cyclic impact studies directly comparing polysilicon and polySiC interfaces. The dependence of stiction on apparent area of contact (as defined by microfabricated dimples) has been quantified for both material systems. It was observed that polycrystalline SiC exhibit adhesion forces less than half those on polysilicon. This work further documents the evolution of morphological, electrical, and chemical properties of polysilicon and polySiC surfaces undergoing cyclic contacts for over 100 billion times. The above approach highlights the beneficial effect of a thin silicon carbide coating on polysilicon by enhancing contact reliability. More specifically, it was seen that SiC coating suppresses the rate of adhesive wear and grain fracture: little change to the SiC surface morphology and suggest wear resistance under relatively high impact forces for over 100 billion cycles; in contrast to polysilicon which exhibited wear debris and crater-like surface features indicative of grain fracture from cyclic loading. The devices remained resilient to failure from stiction due to low surface energy in the presence of SiC coating, and to accelerated mechanical stress due to hardened contacting surfaces. More compliant (longer) devices have been successfully actuated owing to low surface energy nature of SiC coating. This work further demonstrates that surface modification of silicon-based microdevices through thin SiC coating after microstructure release is an effective approach to suppress stiction and wear-related failure mechanisms while avoiding constraints on the front-end fabrication steps.

Acknowledgments

This work was supported in part by the Defense Advanced Research Projects Agency (DARPA) N/MEMS S&T Fundamentals program under grant no. N66001-10-1-4004 issued by the Space and Naval Warfare Systems Center Pacific (SPAWAR). Support from Robert Bosch GmbH and STMicroelectronics is also gratefully acknowledged. The authors also thank Nathan Klejwa for assistance in the AES analysis, Prof. Roger T. Howe (Stanford University) for helpful discussion, Dr. Fang Liu for assistance with the device design and packaging, and Yongkwan Kim for assistance with the AFM/SEM analysis.

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